FIG. 1

Field	Length (bits)
P_REV	8
MIN_P_REV	8
SID	15
NID	16
PILOT_PN	9
LC_STATE	42
SYS_TIME	36
LP_SEC	8
LTM_OFF	6
DAYLT	1
PRAT	2
CDMA_FREQ	11
EXT_CDMA_FREQ	11
SR1_BCCH_NON_TD_INCL	1
SR1_NON_TD_FREQ_INCL	0 or 1
SR1_CDMA_FREQ_NON_TD	0 or 11
SR1_BRAT_NON_TD	0 or 2
SR1_CRAT_NON_TD	0 or 1
SR1_BCCH_CODE_CHAN_NON_TD	0 or 6
SR1_TD_INCL	1
SR1_CDMA_FREQ_TD	0 or 11
SR1_BRAT_TD	0 or 2
SR1_CRAT_TD	0 or 1
SR1_BCCH_CODE_CHAN_TD	0 or 6
SR1_TD_MODE	0 or 2
SR1_TD_POWER_LEVEL	0 or 2
SR3_INCL	1
SR3_CENTER_FREQ_INCL	0 or 1
SR3_CENTER_FREQ	0 or 11
SR3_BRAT	0 or 2
SR3_BCCH_CODE_CHAN	0 or 7
SR3_PRIMARY_PILOT	0 or 2
SR3_PILOT_POWER1	0 or 3
SR3_PILOT_POWER2	0 or 3
DS_INCL	1
DS_BLOB	0 or 24

FIG. 2